

HP6404 IP-to-ASI MUX-Scrambler



Overview

HP6404 IP-to-ASI MUX-Scrambler is a cost effective and high-integrated IP multiplexer and scrambler, supporting 64×IP input and 4×ASI output. The input IP signal will be sent to signal processor after synchronized process, and the processor will be multiplexed and scrambled to output 4×ASI directly. It supports 4×CAS/SMS simulcrypt. It suits 1U rack and can be configured by front panel LCD and NMS (network management software). Its high-integrated and cost-effective design makes it widely used in varieties of digital broadcasting distribution systems.

Features

- ◆ 8M cache, anti-burst stream
- ◆ PID filter and PID re-mapping
- ◆ PSI/SI edit and NIT table inserts
- ◆ Real time monitoring
- ◆ PCR re-adjustment and correction
- ◆ Package format supports 188/204
- ◆ LCD/Keyboard control by front panel and network management by Ethernet

Technical Specifications

Input	IP Port	1000Mbps, 64×multicast IP stream
Output	4×ASI	DVB standard, BNC interface
Multiplexer		from the input 64×multicast IP stream
Scrambler		support 4×CAS/SMS; head-end implementation of DVB simulcrypt (ETSI TS 103 197 V1.4.1)
Management		network RJ-45 10/100M Keyboard + LCD
General Features	Size	482mm×280mm×44mm
	Temperature Range	0~45°C (Operation); -20~80°C (Storage)
	Power	100-240VAC, 50Hz, 25W